

Organizing Committee

- Dr. B.R.Karthikeyan – Prof., ECE Dept
- Dr. L.S.Praveen – Asst. Prof, ECE Dept
- Mrs. Deepthi S. – Asst. Prof, ECE Dept
- Mrs. Vasanthvalli – Asst. Prof, ECE Dept
- Mr. Divya Kiran – Asst. Prof, ECE Dept
- Ms. Prekshitha G – Circuitopia Club

Registration Details

Registration Deadline: 24 Oct 2025
Students: 500/-
Faculty Members: 1000/-

Scan or Click on the QR for the registration form.
Payment details are available in registration form



[Click The link](#)

Be First
Limited 40 Seats!

Five Days Workshop on Smart Embedded Systems: Developing AI Solutions with Raspberry Pi, FPGA, IoT and AWS Cloud Platforms

Organized by
Department of Electronics and Communication Engineering

Date: 27-31 Oct 2025
Time: 10:00 AM to 5:00 PM

**Venue: A206, Ramaiah Technology Campus,
M.S. Ramaiah University of Applied Sciences,
Peenya Industrial Area, Bengaluru**

About Workshop

The five-day hands-on workshop on Smart Embedded Systems: Developing AI Solutions with Raspberry Pi, FPGA, IoT, and Cloud Platforms is organised with the primary objective of bridging the gap between Artificial Intelligence (AI) and embedded system technologies. The workshop offers participants an immersive, practical experience in deploying trained AI models across various real-world platforms, emphasising the integration of AI with edge and cloud computing. The workshop also intends to explore the application of FPGA in the domain of Embedded System Design.

Contact For Query

★ **Faculty Coordinator: Dr. L.S. Praveen, 9535762626**
lspraveen.ec.et@msruas.ac.in

Student Coordinator: Ms. Prekshitha G, 9019845717

Speakers



Speaker 1:

Mr Mohsin Khan, Technical Head & Domain Expert, Corporate Trainer, Cranes Varsity



Speaker 2: Shilpa S, Founder, AARIV Technologies



Speaker 3: Mr. Padmanaban K, Software Enabling & Optimization Engineer, Altera Semiconductor Technology

Schedule of Workshop

Day 1
27 Oct 25

10.00 AM-10:45 AM – Inauguration
11:00 AM-5:00PM – Computer Vision & AI Deployment using Raspberry Pi with Hands-On by Cranes Varsity

Day 2
28 Oct 25

10:00 AM- 5:00PM – Development of IoT Models with ESP32 & Hands-On by AARIV Technologies

Day 3
29 Oct 25

10:00 AM - 5:00 PM – AWS Cloud Integration with IoT modules & Hands-On by AARIV Technologies

Day 4
30 Oct 25

10:00 AM-5:00 PM--Altera SoC FPGAs and Platform designer, Embedded Systems design using Cyclone V SoC FPGAs-Demo and Hands-on by Altera Semiconductor Technology

Day 5
31 Oct 25

10:00 AM-4:00 PM – Altera FPGA AI Suite and OpenVINO tool kit- Compilation and IP creation Flow-Analysis of various reports-Hands on by Altera Semiconductor Technology

04:00 PM-05:00 PM – Valedictory Ceremony

Daily

11:30 AM-11:45AM – Tea Break
01:00PM - 2:00 PM – Lunch